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Application Number:	10091934			
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Title of Invention:	Interconnect-aware methodology for integrated circuit design			
First Named Inventor:	Amir Alon			
Correspondence Address:	IBM CORPORATION INTELLECTUAL PROPERTY LAW DEPT. P.O. BOX 218 - YORKTOWN HEIGHTS NY 10598 US 9149453197 -			
Filer:	Stephen Carl Kaufman/Jeanne Jordan			
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages			
1		IL920020007US1RESOA8-8 -06wdeclaration.pdf	492500	yes	12			
	Multipart Description							
	Doc Desc		Start	End				
	Amendment After Final		1	1				
	Claims		2	2				
	Applicant Arguments/Remarks Made in an Amendment		3	8				
	Oath or Declaration filed		9	12				
Warnings:								
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.